IN THE TITLE

Please replace the current title, "Diagonal Wiring Architecture For Integrated Circuit" with "Simulating Euclidean Wiring Directions Using Manhattan And Diagonal Directional Wires."

IN THE SPECIFICATION

On page 1, line 1, please insert--

Ш

CROSS REFERENCE TO RELATED APPLICATIONS

This application is a continuation application of United States Patent Application Ser. No. 09/681,776, entitled "Diagonal Wiring Architecture For Integrated Circuit", filed on June 3, 2001, which is a continuation application of United States Patent Application Serial No. 09/733,104, entitled "Multi-Directional Wiring On A Single Metal Layer", filed on December 7, 2000.

Please delete paragraphs [0004], [0005], and [0006].